



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
Applicant: Gregory E. Howard, et al  
Serial No: 09/967,187  
Examiner: Ida M. Soward  
Filed: 09/28/2001  
For: BIPOLAR JUNCTION TRANSISTOR

Docket No: TI-29894  
Conf. No: 5978  
Art Unit: 2822

RECEIVED  
OCT 23 2002  
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**NOTICE OF APPEAL FROM THE PRIMARY EXAMINER  
TO THE BOARD OF APPEALS**

Assistant Commissioner for Patents  
Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)  
I hereby certify that the above correspondence is being  
deposited with the U.S. Postal Service with sufficient  
postage as First Class Mail in an envelope addressed to:  
Assistant Commissioner for Patents, Washington, DC  
20231 on 10-11-02.

Ann Trent  
Ann Trent

Dear Sir:

Applicants hereby appeal to the Board of Appeals from the decision of the Primary Examiner mailed July 15, 2002, finally rejecting Claims 1-9.

The item(s) checked below are appropriate:

1. ☐ An extension of time to respond to the final rejection  
☐ was granted on \_\_\_\_\_.  
☐ is requested for \_\_\_\_ month.
2. ☒ A timely response to the final rejection has been filed.
3. ☒ Fee \$320.00:

☐ Not required (Fee paid in prior appeal)  
☒ The Commissioner of Patents is hereby authorized to charge any fees which may be required or credit any overpayment to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.  
**This form is submitted in triplicate.**

Texas Instruments Incorporated  
P.O. Box 655474, MS 3999  
Dallas, TX 75265  
(972) 917-4258

Peter K. McLarty  
Peter K. McLarty  
Agent for Appellants  
Reg. No. 44,923